

ABSTRACT OF THE DISCLOSURE

5      Method for tuning a PLL circuit

In the previously known methods for tuning the PLL circuit, the loop filter has to be discharged again if the PLL circuit does not lock in. During this time, also known as dead time, the PLL circuit cannot detect any signals.

10     With the new method, the PLL circuit can also detect signals during the discharge period of the loop filter. This avoids dead times and the signal detection by the PLL circuit is substantially accelerated. The data throughput is increased, especially when used in high-frequency applications such as, for example, in the field of mobile telephones.